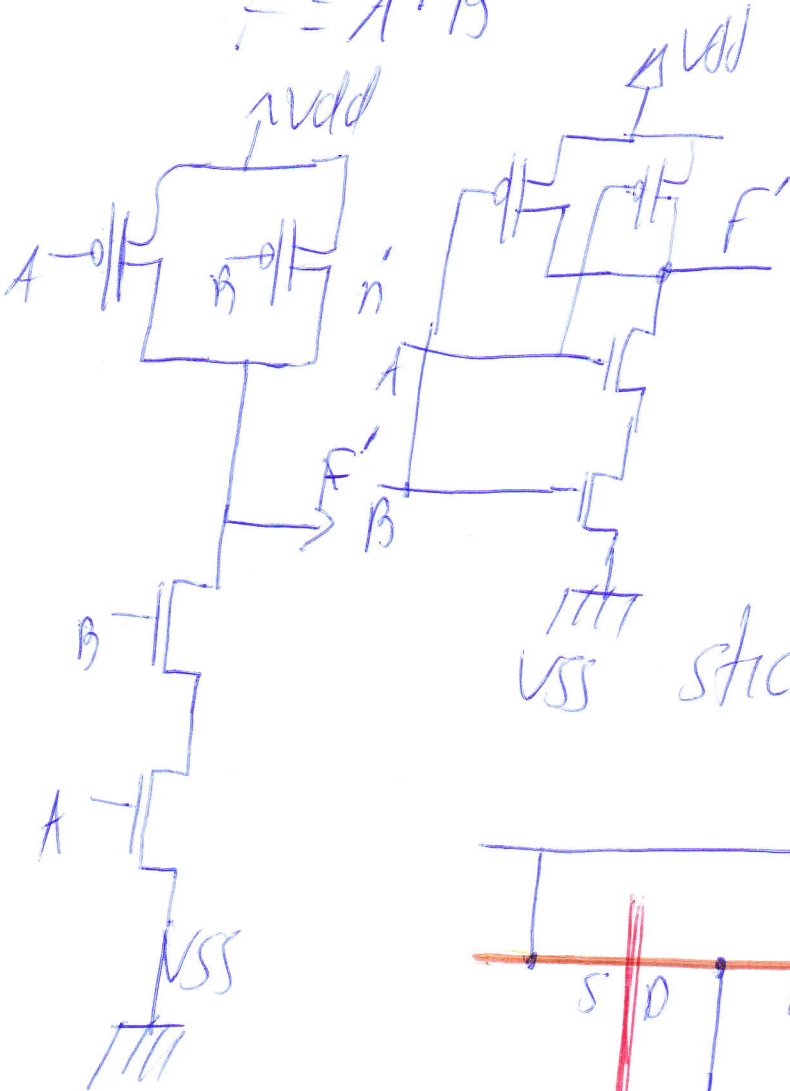
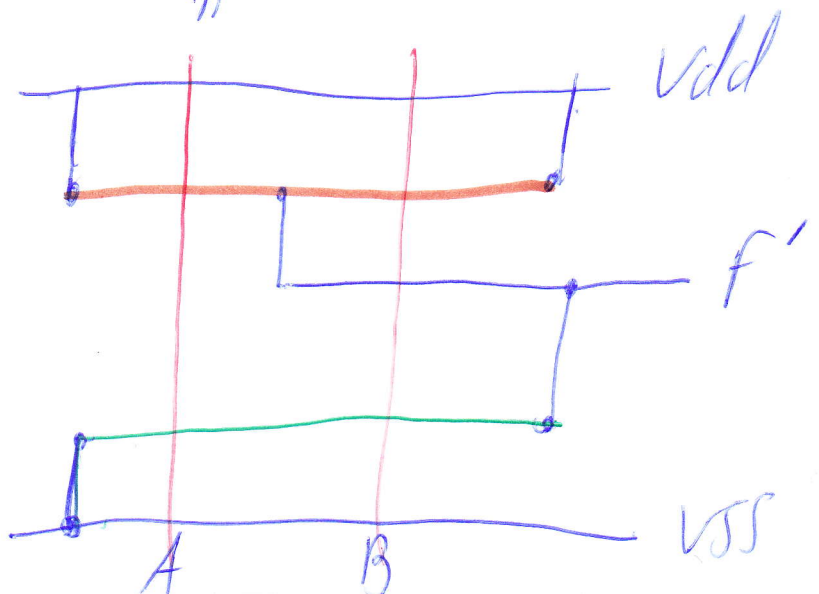
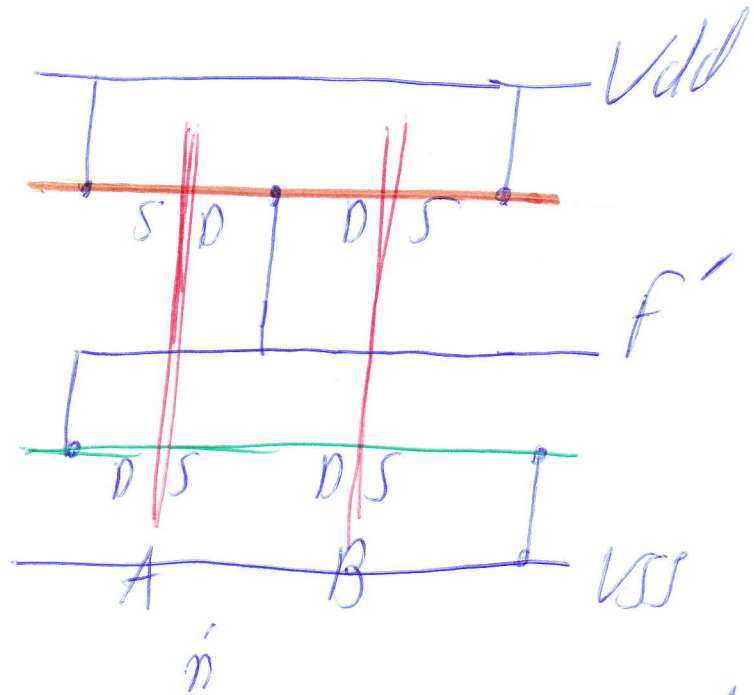


$$F = \overline{A \cdot B}$$

NAND gate



stick diagram



$$F = \overline{A+B}$$

NOR

