



# Liquid Crystal Display (LCD) Passive Matrix and Active Matrix Addressing

### Introduction

Flat panel displays are now challenging CRT as the display of choice in many applications. Liquid Crystal Displays (LCDs), both passive matrix and active matrix technologies have been developed to replace the once CRT dominated displays market. Passive matrix display costs are lower, but their performance is inferior to active matrix.

This application note examines the two addressing techniques used in current LCDs.

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### **1 Passive Matrix**

Passive matrix displays have rows of electrodes on one half of the display glass and columns of electrodes on the other. The electrodes are usually fabricated out of Indium Tin Oxide (ITO), a semi-transparent metal oxide. When the two pieces of glass are assembled into a display, the intersection of a row and column form a pixel element (*Figure 1*). When a voltage is applied between the two points, the crystal realigns and changes the light transmission properties of the liquid crystal. By repeating this process, scanning through the pixels an image can be formed on the display.

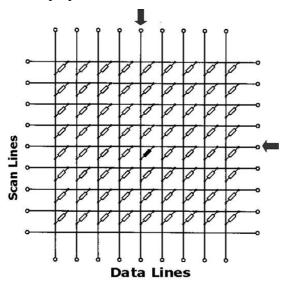


Figure 1: Passive Matrix Addressing

Problems arise as the number of rows and columns increase. With higher pixel density, the electrode size must be reduced and the amount of voltage necessary to drive the display rapidly increases. The higher driving voltage creates a secondary problem; charging effects. Even though only one row and column are selected, the liquid crystal material near the row and column being charged are affected by the pulse. The net result is the pixel selected is active (dark), but the areas surrounding the addressed point are also partially active (greys). The partially active pixels reduce the display contrast and degrade image quality. This effect is known as cross talk.

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## 2 Active Matrix

Passive matrix displays suffer from fundamental problems leading to poor performance. Matrix addressing solves these problems. By placing an electronic switch device in each LC pixel (*Figure 2*), controlling the charging of the LC cell to the desired grey level.

	C <sub>1</sub>	C <sub>2</sub>	C <sub>3</sub>	C <sub>4</sub>	
R <sub>1</sub>			LC LC SWITCH		
R <sub>2</sub>					
R <sub>3</sub>	LC - C - C - C - C - C - C - C - C - C -	LC⊥ -ŢŹ」- SWITCH	LC - Z - SWITCH	LC LC SWITCH	(
R <sub>4</sub>					

#### Figure 2: Active Matrix Addressing

There are a number of available technologies which can be used to construct the electronic switches; the materials for which are shown in *Table 1*.

Active elements 2T	Active elements 3T
MIM	a-Si:H TFT
a-Si:H diode	p-Si TFT
	c-Si (SOI)
	CdSe TFT
	MOS (wafer)

#### **Table 1: Liquid Crystal Switching Materials**

The active devices can be a-Si:H diodes, ring diodes, or MIM diodes, but more usually (for high resolution displays) thin film transistors. There are a number of transistor technologies, including Cadmium Selenide (CdSe), but the most important are hydrogenated amorphous silicon (a-Si:H) and poly-silicon (p-Si). An active switch is placed in each pixel of a LCD that controls the charging of the LC capacitance to the voltage corresponding to the desired grey level, and storing it there until the next frame refresh. This is done usually by the "one line at a time" method of addressing.



AMLCD (Active Matrix Liquid Crystal Display) technology is more expensive but solves the scanning limitations of passive displays, contrast ratio and grey scale, and the flicker induced by holding signals in pixels in a multiplexed system.

The diagram below (*Figure 3*) shows how thin film transistors are incorporated into a matrix isolating the pixels. The gates are connected together in rows called gate lines or buses, and the data lines are connected to the transistor sources. The liquid crystal, which is sandwiched between two transparent metals (Indium Tin Oxide (ITO)), is represented as a capacitor.

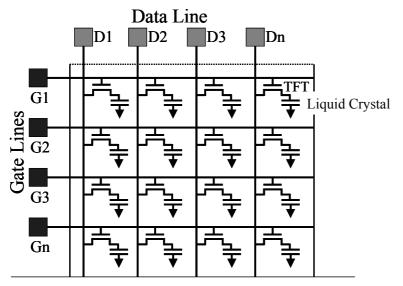


Figure 3: Active Matrix Thin Film Transistor (TFT)

The dominant active matrix technology is thin-film transistors (TFTs) of either a-Si or p-Si. The lower process temperature (<400 degrees centigrade) of a-Si has led to its initial dominance in large-size AMLCD. The present manufacturing of p-Si LCD displays is confined to small-size, usually quartz substrates using high-temperature processes.

## 3 Summary

Active matrix LCD has been proven to be a superior technology in terms of optical performance; however this comes at a price. Therefore with continued improvement in passive matrix LCD technology and low price compared to AMLCD, passive matrix LCD will continue to be used in many applications and outsell AMLCD for the foreseeable future.





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